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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:
Bernacchia et al.

MMB Docket No. **1890-0071**

Application No. **10/804,860**

Filed: **March 19, 2004**

For: **Voltage Regulator with Adjustable**
Output Impedance

Examiner: **To be assigned**

Group Art Unit: **2838**

I hereby certify that this correspondence is being deposited
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James D. Wood

Name of person mailing Document or fee

Signature

March 7, 2005

Date of Signature

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, Applicants hereby disclose the following reference
regarding the above-identified patent application.

Patent References

U.S. Patent No.

6,229,292 B1

6,069,471

Inventor

Redl et al.

Nguyen

Issue Date

May 8, 2001

May 30, 2000

Articles

- 1) Lenk, R., "Understanding Droop and Programmable Active Droop", Application Bulletin AB-24, Fairchild Semiconductor, 2000, (8 pages).
- 2) Gadoura et al., "New Methodology for Design, Analysis, and Validation of DC/DC Converters Based on Advanced Controllers," The 21st International Telecommunications Energy Conference, IEEE, 1999, (7 pages).

U.S. Patent No. 6,229,292 and the above article were cited in a German Office Action dated April 27, 2004 (copy enclosed) of the German priority application DE 103 12 221.4 filed on March 19, 2003.

Pursuant to 37 C.F.R. § 1.97(b), this Information Disclosure Statement is being filed within three months after the filing date of the application or before the mailing of the first office action on the merits.


It is believed that no fees are due for the consideration of this Information Disclosure Statement. However, the Commissioner is hereby authorized to charge any deficiency or to credit any overpayment to Deposit Account No. 13-0014, but not to include any payment of issue fees.

March 7, 2005
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Respectfully Submitted,



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FORM PTO-1449 <u>INFORMATION DISCLOSURE STATEMENT</u> 	MMB DOCKET NO. 1890-0071	APPLICATION NO.: 10/804,860
	APPLICANT(S): Bernacchia et al.	
	FILING DATE: March 19, 2004	GROUP ART UNIT: 2838

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA	6,229,292 B1	May 8, 2001	Redl et al.			
	AB	6,069,471	May 30, 2000	Nguyen			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL						Yes No
	AM						Yes No
	AN						Yes No
	AO						Yes No
	AP						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AQ		Gadoura et al., "New Methodology for Design, Analysis, and Validation of DC/DC Converters Based on Advanced Controllers," The 21st International Telecommunications Energy Conference, IEEE, 1999, (7 pages).
	AR		Lenk, R., "Understanding Droop and Programmable Active Droop", Application Bulletin AB-24, Fairchild Semiconductor, 2000, (8 pages).
	AS		

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicants.